() Preliminary Specifications
(V) Final Specifications

Module	12.1 Inch Color TFT-LCD with touch panel
Model Name	G121EAT02.0

Customer	**Date**	**Approved by**	**Date**
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TP Firmware version is V03

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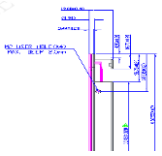
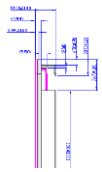
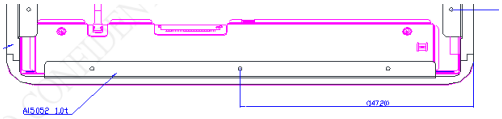
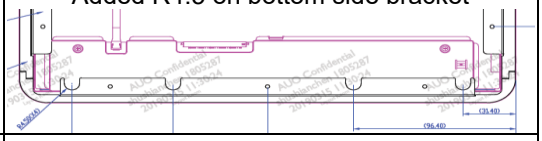
Record of Revision

Version and Date	Page	Old description	New Description
0.0 2018/07/26	All	First Edition	
0.1 2018/07/30	5	TP I/F : I2C	USB
	7	White Luminance (min.) : 280	Update to 290
	22		Update TP structure thickness : Cover Lens : 1.8t Sensor glass : 0.7t OCR 0.45t with LCM lamination
0.2 2018/08/10	22 / 23		Update new drawing
0.3 2018/09/19	23		Update Rear View drawing
	24		Update carton packing information
0.4 2018/11/05	17	TP Connector change to ACES Manufacturer: Panasonic Electric Works. Connector Model Number: AYF530835 Pin NO / Signal Name: 1 GND_E 2 D- 3 D+ 4 I2C_INT 5 I2C_SCL 6 I2C_SDA 7 VDD 8 Check_ID	Manufacturer: ACES Electric Co.Ltd. Connector Model Number: 50224-00501-001 Pin# / Symbol / Signal Name: 1 VCC Power 5V 2 D- USB D- 3 D+ USB D+ 4 GND Ground 5 GND Ground
			Update Front View drawing
0.5 2018/11/07	6	Surface Treatment : AG	w/o AG
0.6 2018/11/07	22		Cancel AG Treatment
0.7 2019/01/04	22/23	Without system bracket	With system bracket
0.8 2019/02/22	22/23	bracket side mount hole 	Cancel bracket side mount hole 
0.9 2019/03/29	22/23	Without R4.5*4 on bottom side bracket 	Added R4.5 on bottom side bracket 
0.10 2020/05/27	23	Poron WP-32P	Poron WP-24P
	1	NA	TP Firmware version is V03
0.11 2025/04/28	6	Weight: 900 (max.)	Weight: 935 (max., include TP)
	8	White Luminance IF= 43.1mA/1 LED Line	White Luminance IF= 52.4mA/1 LED Line
	15	IF = 43.1 mA, Ta = 25 °C	IF = 52.4 mA, Ta = 25 °C

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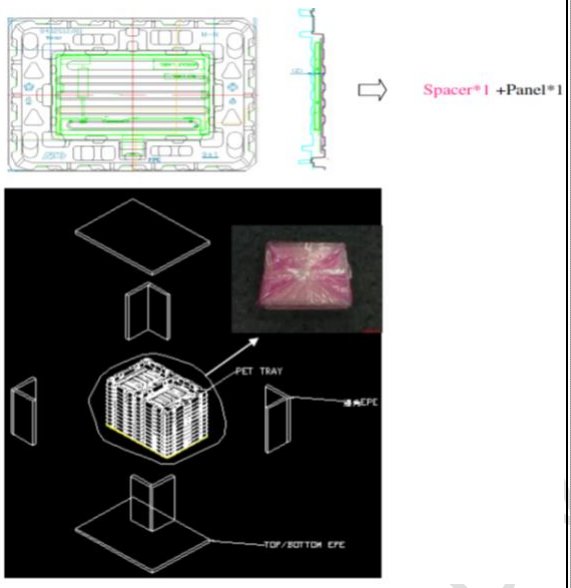
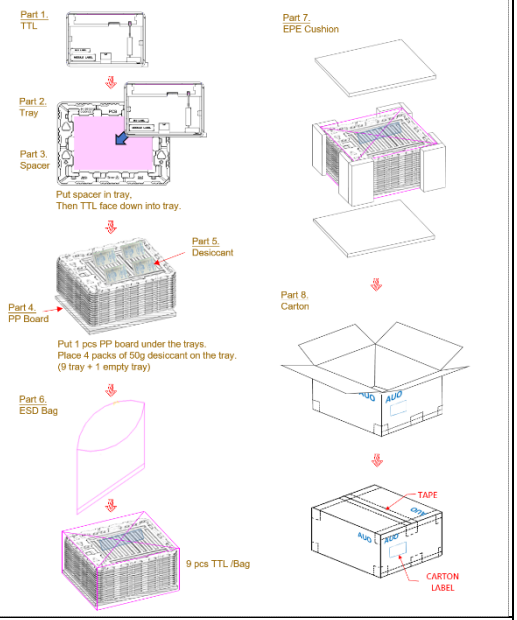
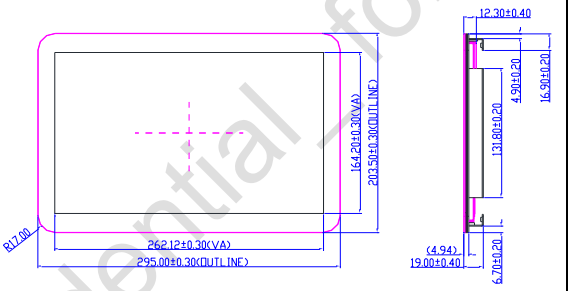
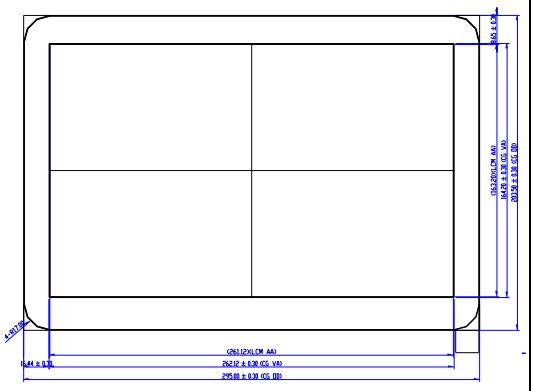
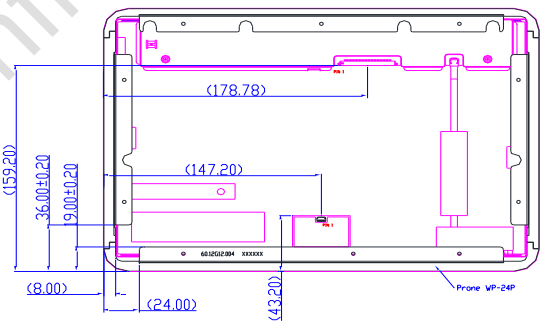
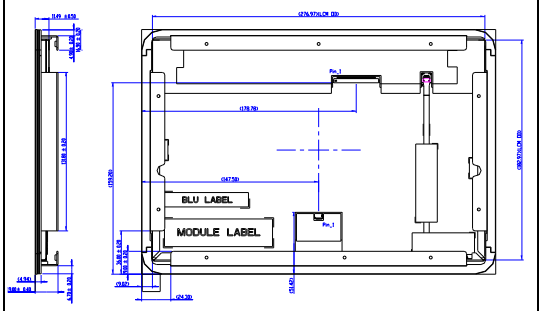
	17	**Connector Name / Designation** | **Signal Connector** Manufacturer | Starconn Connector Model Number | 093G30-B0001A-1	**Connector Name / Designation** | **Signal Connector** Manufacturer | P-TWO INDUSTRIES INC (or compatible) Connector Model Number | 187178-30091-3 (or compatible)
	18	Manufacturer | ACES Electric Co,Ltd. Connector Model Number | 50224-00501-001	Manufacturer | ACES Electric Co,Ltd. (or compatible) Connector Model Number | 50224-00501-001 (or compatible)
	25		
0.12 2025/06/30	23		
	24		

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1. Operating Precautions

1) Since front polarizer is easily damaged, please be cautious and not to scratch it.
2) Be sure to turn off power supply when inserting or disconnecting from input connector.
3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
4) When the panel surface is soiled, wipe it with absorbent cotton or soft cloth.
5) Since the panel is made of glass, it may be broken or cracked if dropped or bumped on hard surface.
6) To avoid ESD (Electro Static Discharge) damage, be sure to ground yourself before handling TFT-LCD Module.
7) Do not open nor modify the module assembly.
8) Do not press the reflector sheet at the back of the module to any direction.
9) In case if a module has to be put back into the packing container slot after it was taken out from the container, do not press the center of the LED light bar edge. Instead, press at the far ends of the LED light bar edge softly. Otherwise the TFT Module may be damaged.
10) At the insertion or removal of the Signal Interface Connector, be sure not to rotate nor tilt the Interface Connector of the TFT Module.
11) TFT-LCD Module is not allowed to be twisted & bent even force is added on module in a very short time. Please design your display product well to avoid external force applying to module by end-user directly.
12) Small amount of materials without flammability grade are used in the TFT-LCD module. The TFT-LCD module should be supplied by power complied with requirements of Limited Power Source (IEC60950 or UL1950), or be applied exemption.
13) Severe temperature condition may result in different luminance, response time and lamp ignition voltage.
14) Continuous operating TFT-LCD display under low temperature environment may accelerate lamp exhaustion and reduce luminance dramatically.
15) The data on this specification sheet is applicable when LCD module is placed in landscape position.
16) Continuous displaying fixed pattern may induce image sticking. It’s recommended to use screen saver or shuffle content periodically if fixed pattern is displayed on the screen.

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2. General Description

This specification applies to the Color Active Matrix Liquid Crystal Display G121EAT02.0 composed of a TFT-LCD display, a driver and power supply circuit, and a LED backlight system.

The screen format is intended to support WXGA (1280(H) x 800(V)) screen and 16.2M (8-bits) or 262k colors (6-bits). LED driving board for backlight unit is included in G121EAT02.0. G121EAT02.0 designed with wide viewing angle; wide temperature and long life LED backlight is well suited for industial applications. G121EAT02.0 is a RoHS product.

G121EAT02.0 has a Projected Capacitive Touch Panel with USB interface to support and compatible with single touch on WinXP O/S, and multi-touch on Win8 O/S or Andorid system.

2.1 Display Characteristics

The following items are characteristics summary on the table under 25 ℃ condition:

Items	**Unit**	**Specifications**
Screen Diagonal	[inch]	12.1
Active Area	[mm]	261.12(H) x 163.2(V)
Pixels H x V		1280 x 3(RGB) x 800
Pixel Pitch	[mm]	0.204 x 0.204
Pixel Arrangement		R.G.B. Vertical Stripe
Display Mode		Normally Black
Nominal Input Voltage VDD	[Volt]	3.3 (typ.)
Typical Power Consumption	[Watt]	6.06 @White Pattern
Weight	[Grams]	935 (max., include TP)
Physical Size	[mm]	295.00(H) x 203.50(V) x11.49(D) (Typ.)
Electrical Interface		1 channel LVDS
Surface Treatment		NA
Support Color		16.2M / 262K colors
Temperature Range Operating Storage (Non-Operating)	 [°C] [°C]	 -30 to +85 -30 to +85
RoHS Compliance		RoHS Compliance

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2.2 Touch Characteristics

Item		Specifications
Type		Projected Capacitive Touch Panel
Structure		Glass / Glass
Panel Size		12.1 inch
Total Thickness		2.65mm ± 0.15 mm (Cover_1.8mm, sensor_0.7mm & OCR_0.15mm)
Input Mode		Multi Finger
Cover Lens	O.D.	295 x 203.5mm
	Thickness	1.8 mm
C/L Visual Area		262.12mm * 164.2 mm
Sensor Glass	O.D.	277 x 175 mm
	Thickness	0.7 mm
TP Active Area		261.12mm * 163.2 mm
Total Weight		400 g
Surface Treatment		NA
Interface		USB
Haze		NA
Gloss		NA
Transparency		88 +/- 3%
Single / Multi-touch Accuracy		Center:1mm Edge 1.5mm
Linearity		Center +/-1mm Edge:+-1.5mm
The smallest distance between 2 points		13mm
Channel (X * Y)		51 * 39
Report Rate (points /sec)		>100Hz

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2.3 Optical Characteristics

The optical characteristics are measured under stable conditions at 25℃ (Room Temperature):

Item	Unit	Conditions	Min.	Typ.	Max.	Remark
White Luminance	[cd/m2]	I_F= 52.4mA/1 LED Line (center point)	290	350	-	Note 1
Uniformity	%	5 Points	75	-	-	Note 2, 3
Contrast Ratio			800	1000	-	Note 4
Response Time	[msec]	Rising	-	-	-	Note 5
	[msec]	Falling	-	-	-	
	[msec]	Raising + Falling	-	25	-	
Viewing Angle	[degree] [degree]	Horizontal (Right) CR = 10 (Left)	80 80	89 89	- -	Note 6
	[degree] [degree]	Vertical (Upper) CR = 10 (Lower)	80 80	89 89	- -	
Color / Chromaticity Coordinates (CIE 1931)		Red x	0.581	0.631	0.681	
		Red y	0.279	0.329	0.379	
		Green x	0.249	0.299	0.349	
		Green y	0.569	0.619	0.669	
		Blue x	0.105	0.155	0.205	
		Blue y	0.004	0.054	0.104	
		White x	0.263	0.313	0.363	
		White y	0.279	0.329	0.379	
Color Gamut	%			72	-	

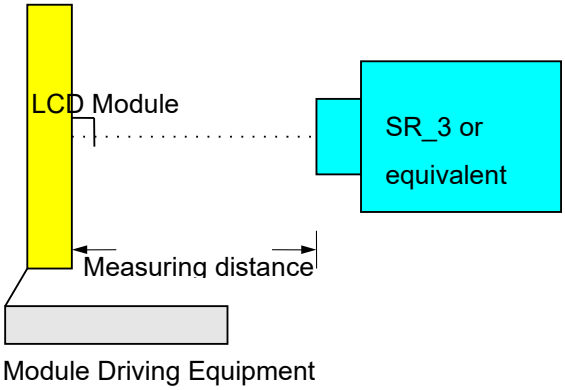
Note 1: Measurement method (with touch panel)

Equipment Pattern Generator, Power Supply, Digital Voltmeter, Luminance meter (SR_3 or equivalent)

Aperture 1◦with 50cm viewing distance

Test Point Center

Environment < 1 *lux*



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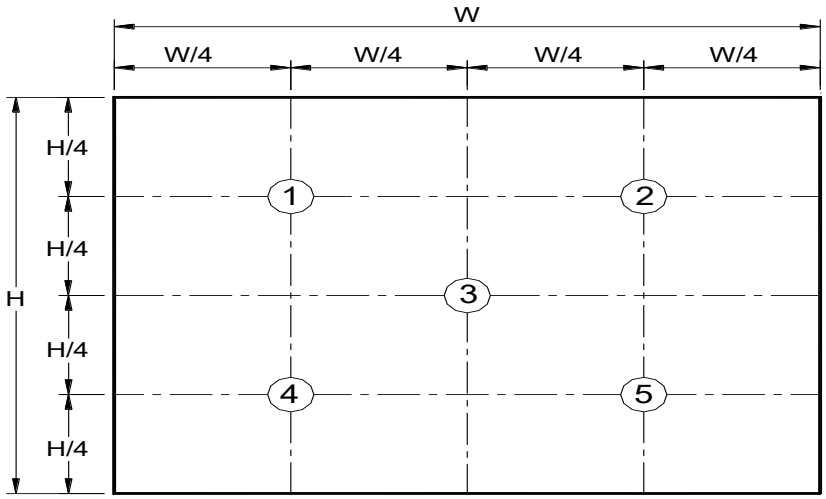
Note 2: Definition of 5 points position (Display active area: 261.12mm (H) x 163.2mm (V))



Note 3: The luminance uniformity of 5 points is defined by dividing the minimum luminance values by the maximum test point luminance

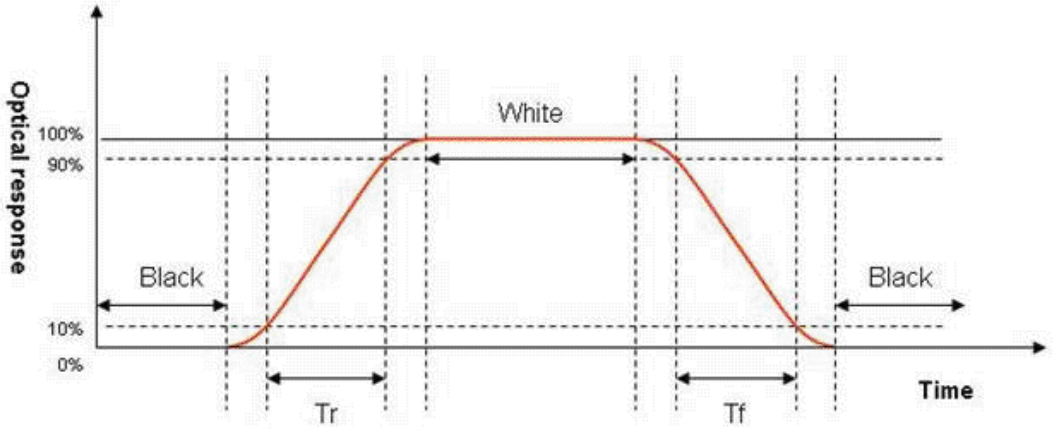
$$\delta_{W9} = \frac{\text{Minimum Brightness of five points}}{\text{Maximum Brightness of five points}}$$

Note 4: Definition of contrast ratio (CR):

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the “White” state}}{\text{Brightness on the “Black” state}}$$

Note 5: Definition of response time:

The output signals of photo detector are measured when the input signals are changed from “White” to “Black” (falling time) and from “Black” to “White” (rising time), respectively. The response time interval is between 10% and 90% of amplitudes. Please refer to the figure as below.



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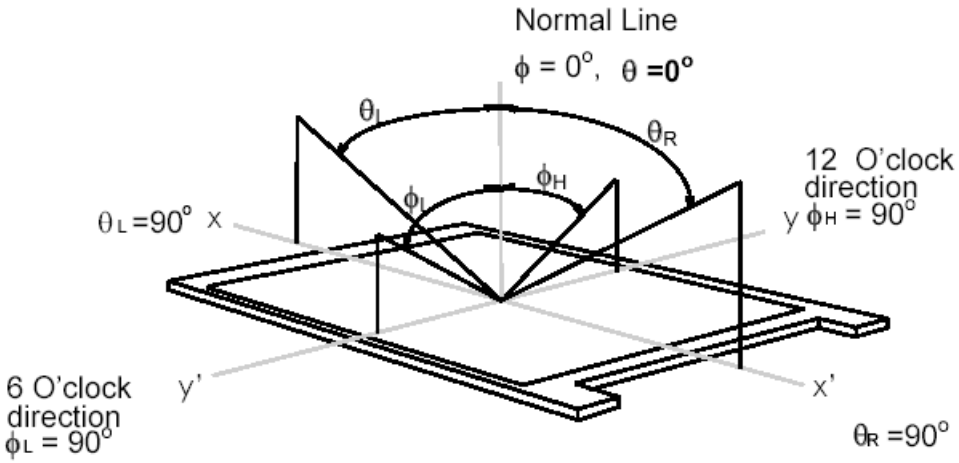
Note 6: Definition of viewing angle

Viewing angle is the measurement of contrast ratio ≧10, at the screen center, over a 180° horizontal and 180° vertical range (off-normal viewing angles). The 180° viewing angle range is broken down as below: 90° (θ) horizontal left and right, and 90° (Φ) vertical high (up) and low (down). The measurement direction is typically perpendicular to the display surface with the screen rotated to its center to develop the desired measurement viewing angle.



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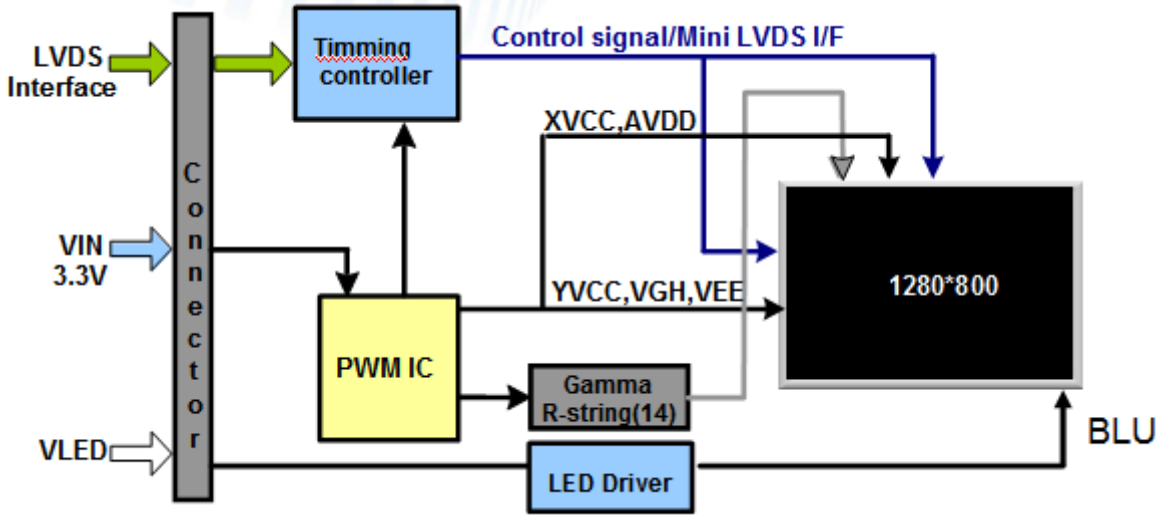
3. Functional Block Diagram

The following diagram shows the functional block of the 12.1 inch color TFT/LCD module:



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4. Absolute Maximum Ratings

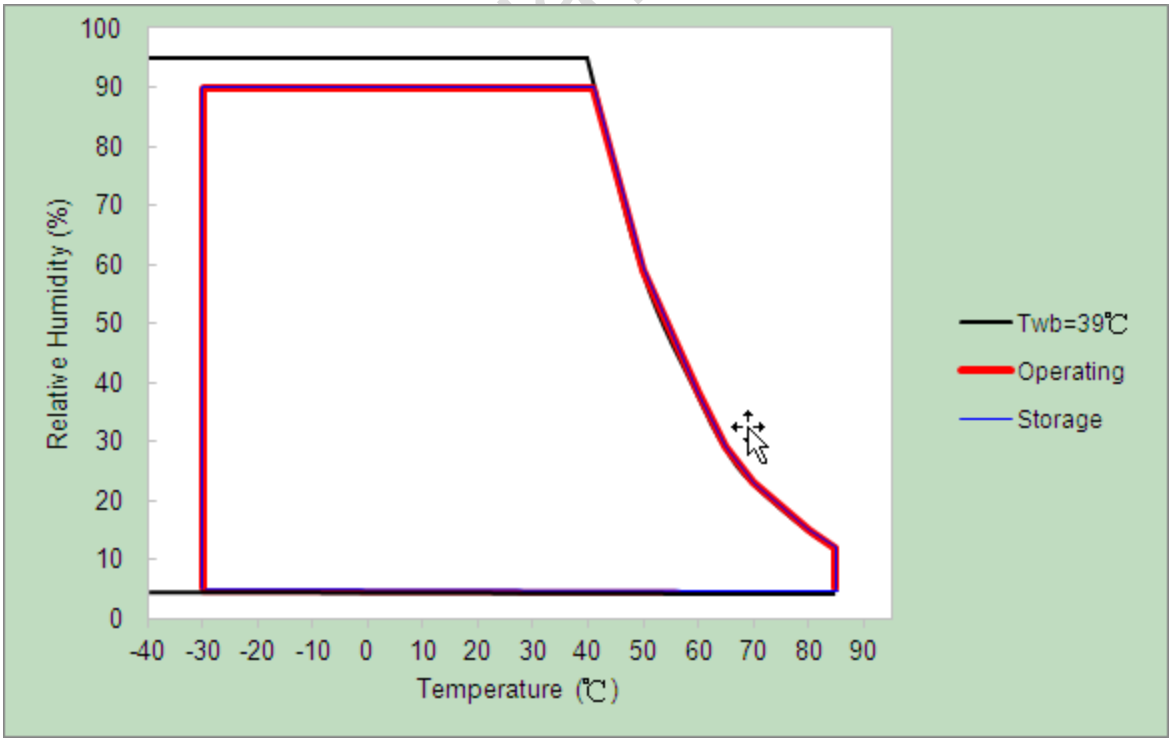
4.1 Absolute Ratings of TFT LCD Module

Item	Symbol	Min	Max	Unit
Logic/LCD Drive Voltage	VDD	-0.3	+3.8	[Volt]
Contorl Signals	SEL68	-0.3	VDD	[Volt]
LVDS Signals	CLKIN -/+, RIN0 ~ 3-/+	-0.3	0.6	[Volt]
LED Input Voltage	VLED	-0.3	15	[Volt]
LED Control Signals	EN, PWM	-0.3	+6	[Volt]

4.2 Absolute Ratings of Environment

Item	Symbol	Min	Max	Unit
Operating Temperature	TOP	-30	+85	[°C]
Operation Humidity	HOP	5	90	[%RH]
Storage Temperature	TST	-30	+85	[°C]
Storage Humidity	HST	5	90	[%RH]

Note: Maximum Wet-Bulb should be 39℃ and no condensation.



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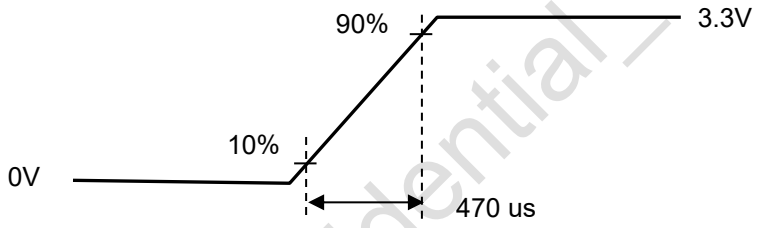
5. Electrical Characteristics

5.1 TFT LCD Module

5.1.1 LCD Electrical Specification

Symbol	Parameter		Min	Typ	Max	Units	Remark
VDD	Logic/LCD Input Voltage		3.0	3.3	3.6	[Volt]	
I_{VDD}	LCD Input Current		-	-	500	[mA]	VDD=3.3V at 60 HZ, all White Pattern
P_{VDD}	LCD Power comsumption		-	-	1.65	[Watt]	VDD=3.3V at 60 HZ, all White Pattern
$I_{rush\ LCD}$	LCD Inrush Current		-	-	1.5	[A]	VDD=3.3V at 60 HZ, all White Pattern Note 1 VDD rising time≧470us
VDD_{rp}	Allowable Logic/LCD Drive Ripple Voltage		-	-	100	[mV] p-p	VDD=3.3V at 60 HZ, all Black Pattern
VIH	Control Signals Voltage	High Level	0.7* VDD	-	VDD	[Volt]	SEL68
VIL		Low Level	0	-	0.3* VDD	[Volt]	SEL68

Note 1: Measurement condition:



VDD rising time

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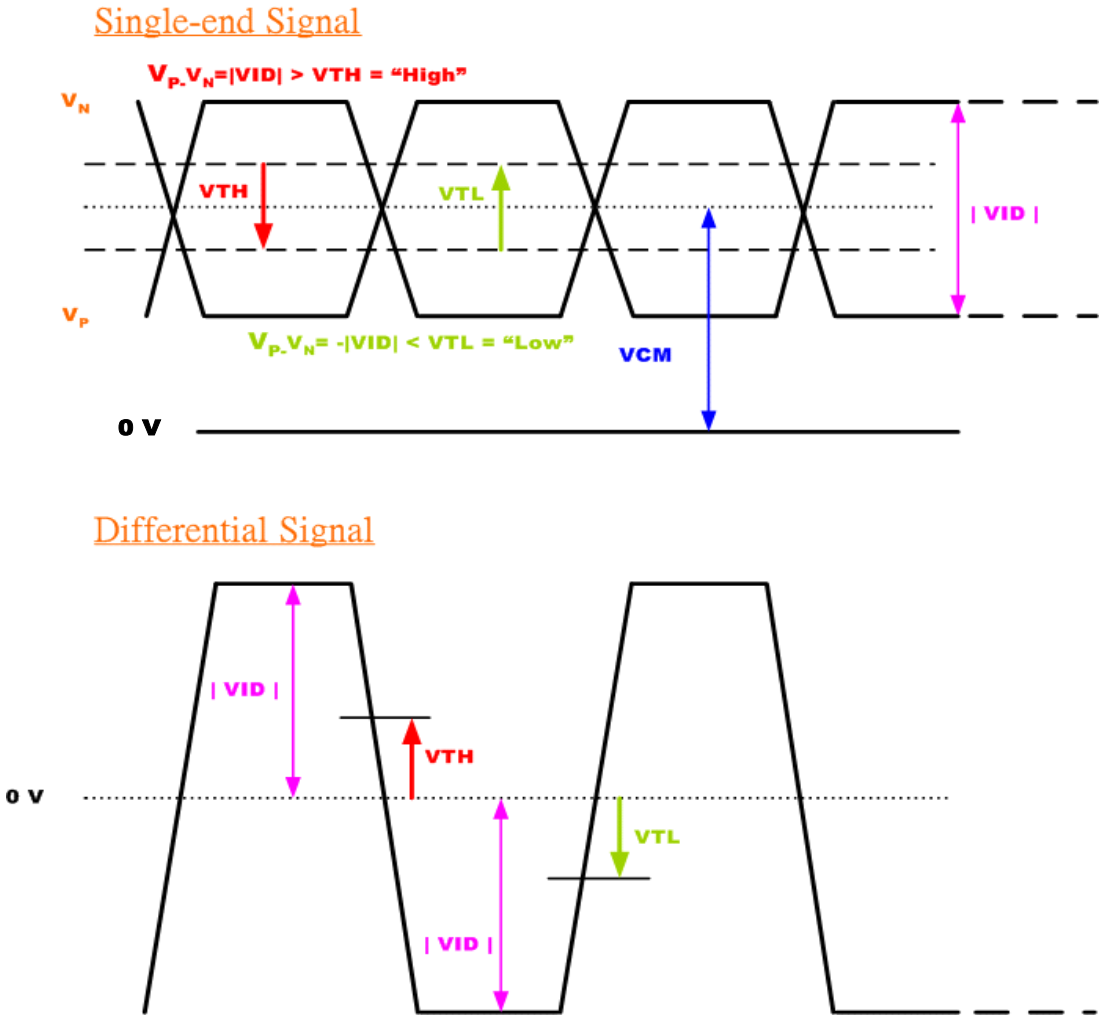
Symbol	Parameter	Min.	Typ.	Max.	Unit	Remark
VTH	Differential Input High Threshold	-	-	100	[mV]	VCM=1.2V
VTL	Differential Input Low Threshold	-100	-	-	[mV]	VCM=1.2V
| VID |	Input Differential Voltage	100	400	600	[mV]	
VICM	Differential Input Common Mode Voltage	1.1	-	1.45	[V]	VTH/VTL=+-100mV

5.1.2 LVDS Electrical Characteristics

Input signals shall be low or Hi-Z state when VDD is off.

Note: LVDS Signal Waveform.



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5.2 Backlight Unit

5.2.1 Parameter guideline for LED

Following characteristics are measured under stable condition at 25℃. (Room Temperature):

Symbol	Parameter		Min.	Typ.	Max.	Unit	Remark
VLED	Input Voltage		10	12	15	[Volt]	
I_{VCC}	Input Current		-	0.37	0.41	[A]	VCC=12V & 100% PWM Duty
P_{VCC}	Power Consumption		-	4.41	4.83	[Watt]	VCC=12V & 100% PWM Duty
V_{EN}	EN Control Level	BL On	2.5	-	4	[Volt]	
		BL Off	0	-	0.5	[Volt]	
F_{PWM}	Dimming Frequency		200	-	20K	[Hz]	
D_{PWM}	Dimming Duty Cycle		1	-	100	%	1~5% with non-linear
V_{PWM}	Dimming Control Level	High Level	3	-	5.5	[Volt]	
		Low Level	0	-	0.5	[Volt]	
LED life time			50,000		-	Hrs	I_F = 52.4 mA, Ta = 25°C

Note 1: Ta means ambient temperature of TFT-LCD module.

Note 2: VCC, I_{VCC}, P_{VCC} are defined for LED backlight.(100% duty of PWM dimming)

Note 3: If G121EAT02.0 module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

Note 4: Operating life means brightness goes down to 50% initial brightness. Minimum operating life time is estimated data.

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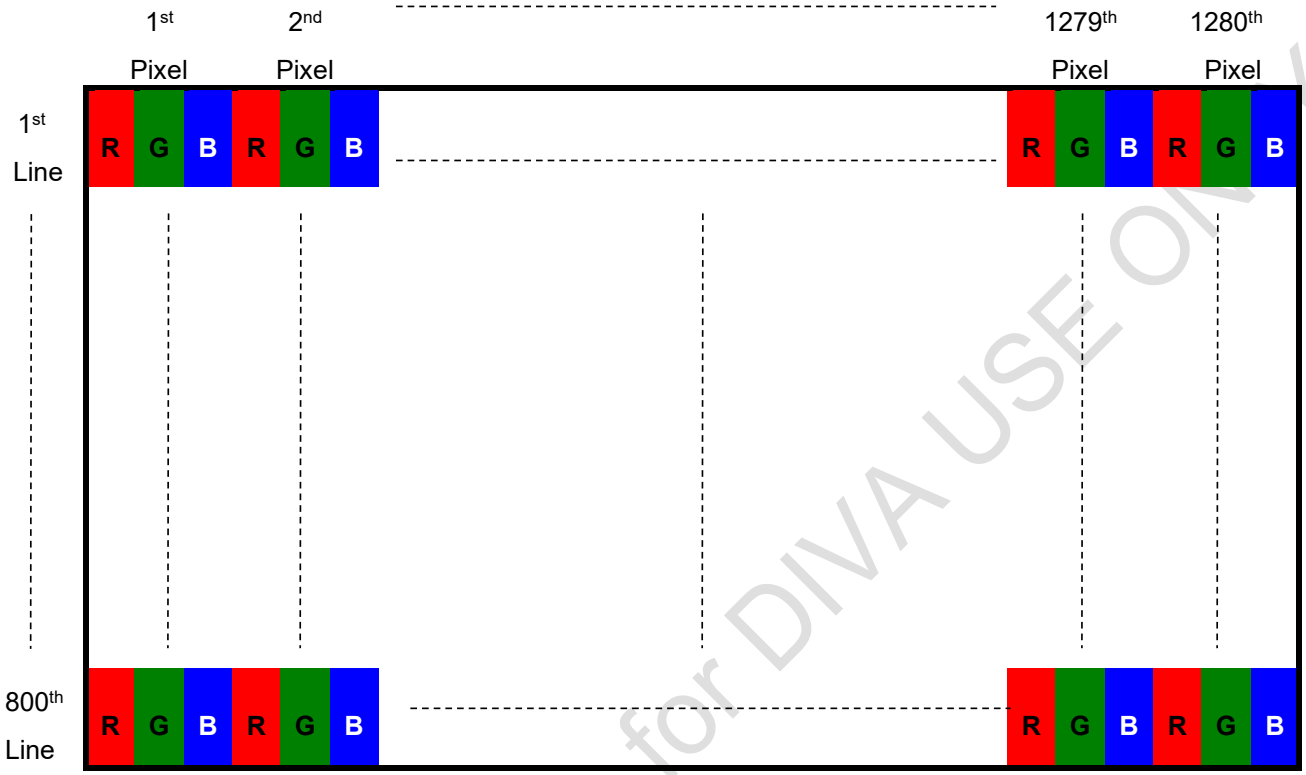
6. Signal Characteristics

6.1 Pixel Format Image

Following figure shows the relationship between input signal and LCD pixel format.



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6.2 Signal Description

The module using a LVDS receiver embaded in AUO's ASIC. LVDS is a differential signal technology for LCD interface and a high-speed data transfer device.

6.2.1 TFT LCD Module: LVDS Connector

Connector Name / Designation	Signal Connector
Manufacturer	P-TWO INDUSTRIES INC (or compatible)
Connector Model Number	187178-30091-3 (or compatible)

6.2.2 Input Signal Description

Pin NO	Signal Name	Description
1	VLED	LED Power Input
2	VLED	LED Power Input
3	VLED	LED Power Input
4	VLED	LED Power Input
5	EN	LED Driver Enable
6	PWM	LED Driver Backlight Adjust
7	GND	Ground
8	GND	Ground
9	VDD	Power supply:+3.3V
10	VDD	Power supply:+3.3V
11	GND	Ground
12	GND	Ground
13	RXin0N	-LVDS differential data (0N)
14	RXin0P	+LVDS differential data (0P)
15	GND	Ground
16	RXin1N	-LVDS differential data (1N)
17	RXin1P	+LVDS differential data (1P)
18	GND	Ground
19	RXin2N	-LVDS differential data (2N)
20	RXin2P	+LVDS differential data (2P)
21	GND	Ground
22	LVDS_RX_N	-LVDS differential clock input
23	LVDS_RX_P	+LVDS differential clock input
24	GND	Ground
25	RXin3N	-LVDS differential data (3N)
26	RXin3P	+LVDS differential data (3P)
27	GND	Ground
28	SEL 6/8	Low or NC-->6 bit input mode High-->8 bit input mode
29	GND	Ground
30	GND	Ground

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6.2.3 TP Module: Connector

Manufacturer	ACES Electric Co,Ltd. (or compatible)
Connector Model Number	50224-00501-001 (or compatible)

Pin#	Symbol	Signal Name
1	VCC	Power 5V
2	D-	USB D-
3	D+	USB D+
4	GND	Ground
5	GND	Ground

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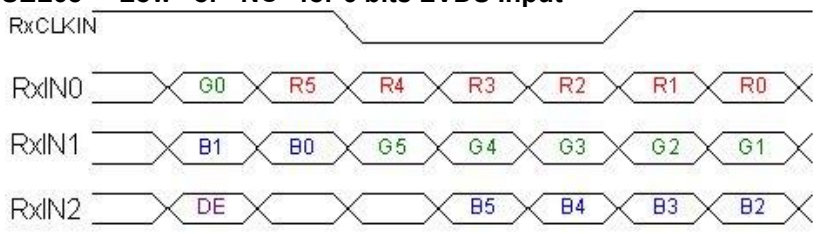
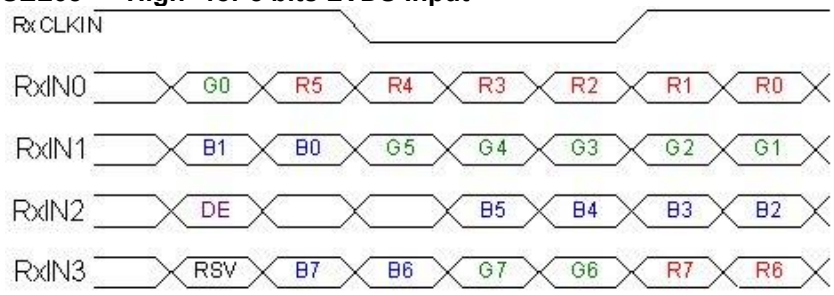
6.3 The Input Data Format

6.3.1 SEL68

SEL68 =“Low” or “NC” for 6 bits LVDS Input



SEL68 = “High” for 8 bits LVDS Input



Note 1: Please follow PSWG.
Note 2: R/G/B data 7:MSB, R/G/B data 0:LSB

Signal Name	Description	Remark
R7 R6 R5 R4 R3 R2 R1 R0	Red Data 7 Red Data 6 Red Data 5 Red Data 4 Red Data 3 Red Data 2 Red Data 1 Red Data 0	Red-pixel Data For 8Bits LVDS input MSB: R7 ; LSB: R0 For 6Bits LVDS input MSB: R5 ; LSB: R0
G7 G6 G5 G4 G3 G2 G1 G0	Green Data 7 Green Data 6 Green Data 5 Green Data 4 Green Data 3 Green Data 2 Green Data 1 Green Data 0	Green-pixel Data For 8Bits LVDS input MSB: G7 ; LSB: G0 For 6Bits LVDS input MSB: G5 ; LSB: G0
B7 B6 B5 B4 B3 B2 B1 B0	Blue Data 7 Blue Data 6 Blue Data 5 Blue Data 4 Blue Data 3 Blue Data 2 Blue Data 1 Blue Data 0	Blue-pixel Data For 8Bits LVDS input MSB: B7 ; LSB: B0 For 6Bits LVDS input MSB: B5 ; LSB: B0
RxCLKIN	LVDS Data Clock	The typical frequency is 40MHz.
DE	Data Enable Signal	When the signal is high, the pixel data shall be valid to be displayed.

Note: Output signals from any system shall be low or Hi-Z state when VDD is off.

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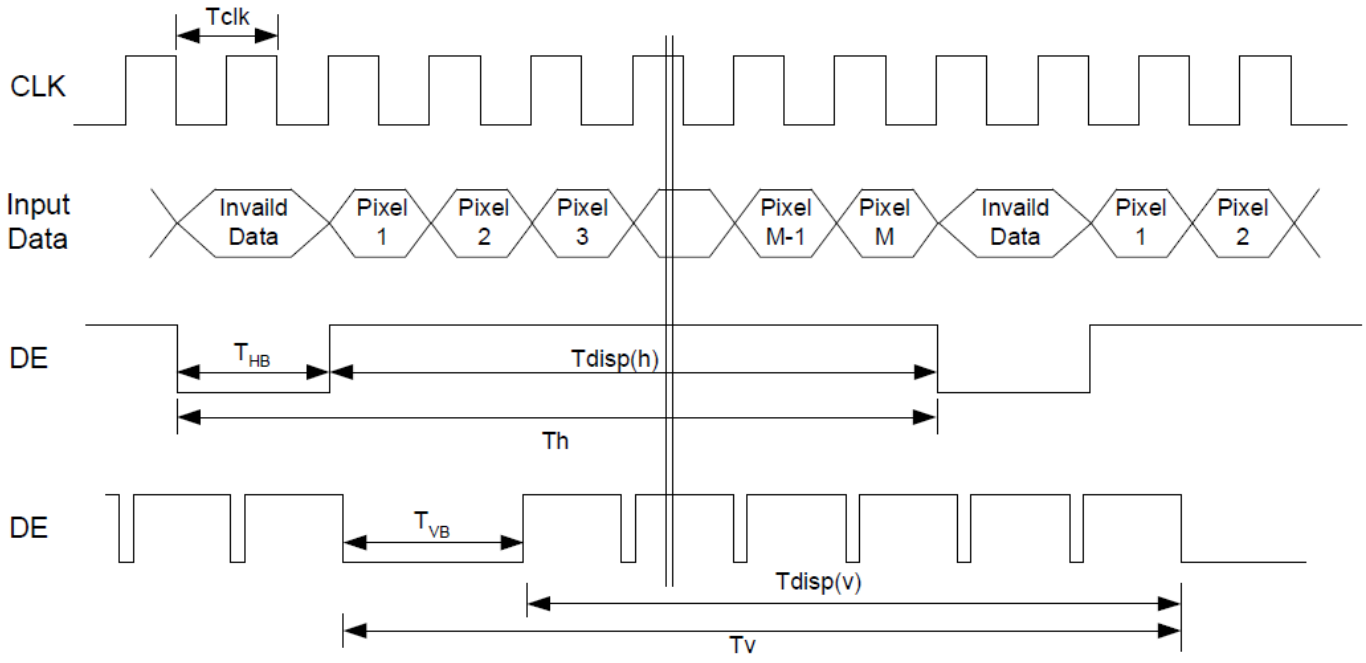
6.4 TFT-LCD Interface Timing

6.4.1 Timing Characteristics

Signal	Parameter		Symbol	Min.	Typ.	Max.	Unit
Clock Timing	Clock frequency		1/ T_{Clock}	60	74.4	90	MHz
Vsync Timing	Vertical Section	Period	T_V	808	838	900	Vsync Timing
		Active	T_{VD}	-	800	-	
		Blanking	T_{VB}	8	38	100	
Hsync Timing	Horizontal Section	Period	T_H	1350	1480	1680	Hsync Timing
		Active	T_{HD}	-	1280	-	
		Blanking	T_{HB}	70	200	400	
Frame Rate			F	50	60	75	Hz

6.4.2 Input Timing Diagram



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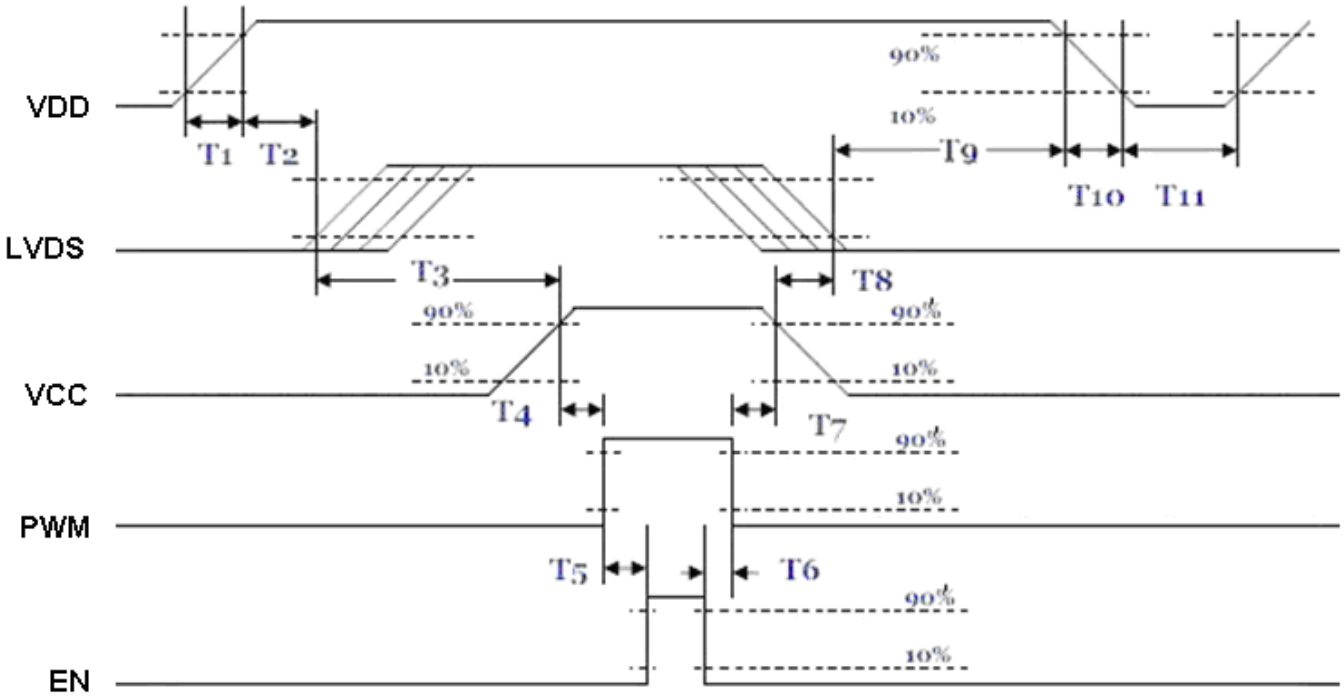
6.5 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Power ON/OFF sequence timing

Parameter	Value			Units
	Min.	Typ.	Max.	
T1	0.5	-	10	[ms]
T2	30	40	50	[ms]
T3	175	-	-	[ms]
T4	10	-	-	[ms]
T5	10	-	-	[ms]
T6	0	-	-	[ms]
T7	10	-	-	[ms]
T8	100	-	-	[ms]
T9	0	16	50	[ms]
T10	-	-	10	[ms]
T11	1000	-	-	[ms]

The above on/off sequence should be applied to avoid abnormal function in the display. Please make sure to turn off the power when you plug the cable into the input connector or pull the cable out of the connector.

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7. Reliability Test Criteria

Items	Required Condition	Note
Temperature Humidity Bias	60 °C, 90%RH, 300 hours	
High Temperature Operation	85 °C, 300 hours	
Low Temperature Operation	-30 °C, 300 hours	
Hot Storage	85 °C, 300 hours	
Cold Storage	-30 °C, 300 hours	
Thermal Shock Test	-20 °C / 30 min, 60 °C / 30 min, 100cycles, 40 °C minimun ramp rate	
Hot Start Test	85 °C / 1Hr min. power on/off per 5 minutes, 5 times	
Cold Start Test	-30 °C / 1Hr min. power on/off per 5 minutes, 5 times	
Shock Test (Non-Operating)	50G, 20ms, Half-sine wave, (±X, ±Y, ±Z)	
Vibration Test (Non-Operating)	1.5G, (10~200Hz, Sine wave) 30 mins/axis, 3 direction (X, Y, Z)	
On/off test	On/10 sec, Off/10 sec, 30,000 cycles	
ESD	Contact = ± 8 kV, class B (R=330,C=150pF) Air = ± 15 kV, class B (R=330,C=150pF) 1sec, 8 points, 25times/point	Note 1
EMI	30-230 MHz, limit 40 dBu V/m, 230-1000 MHz, limit 47 dBu V/m	

Note1: According to EN61000-4-2, ESD class B: Some performance degradation allowed. No data lost Self-recoverable. No hardware failures.

Note2:

- Water condensation is not allowed for each test items.
- Each test is done by new TFT-LCD module. Don't use the same TFT-LCD module repeatedly for reliability test.
- The reliability test is performed only to examine the TFT-LCD module capability.
- To inspect TFT-LCD module after reliability test, please store it at room temperature and room humidity for 24 hours at least in advance.
- In the standard condition, there is not display function NG issue occurred.

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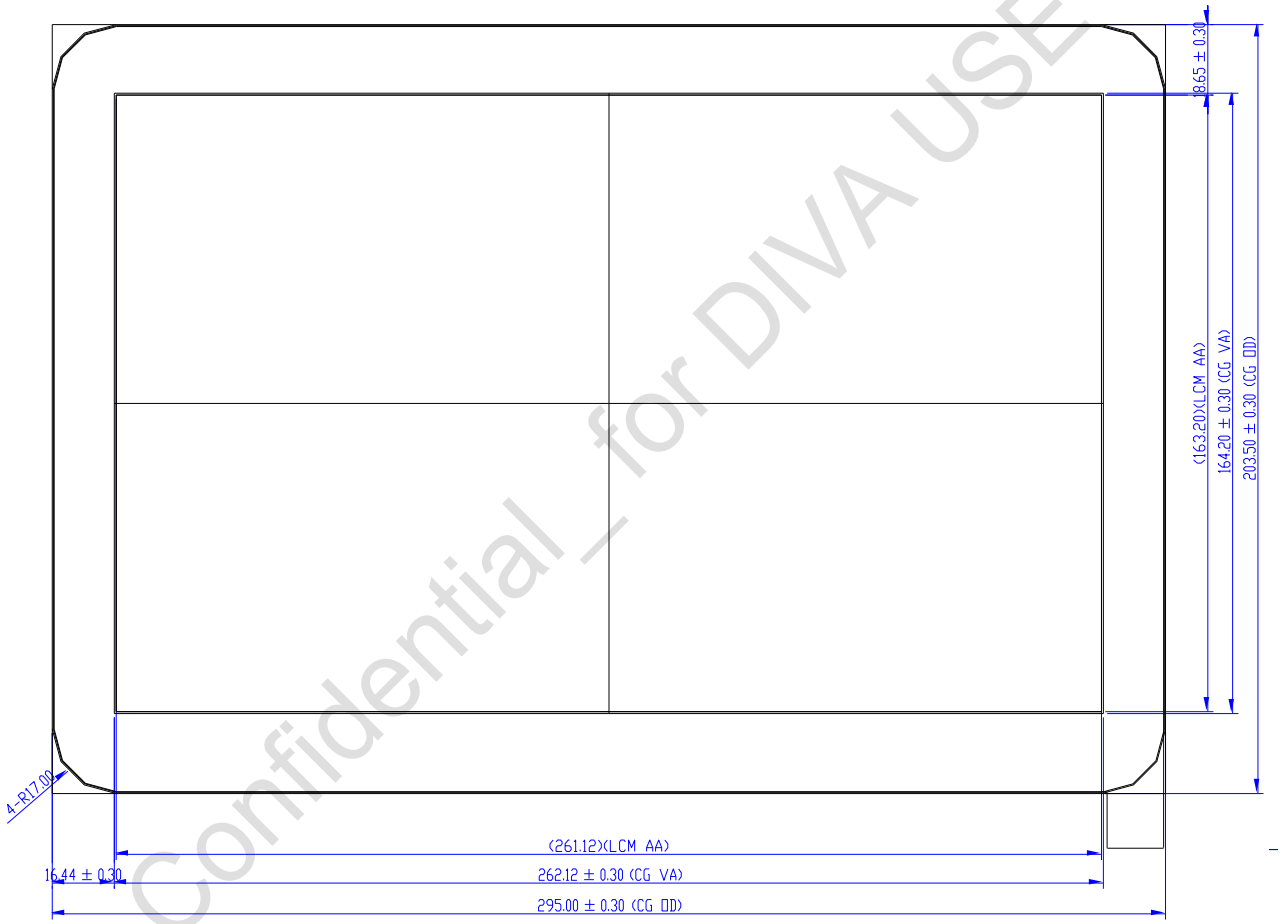
8. Mechanical Characteristics

8.1 LCM Outline Dimension (Front View)



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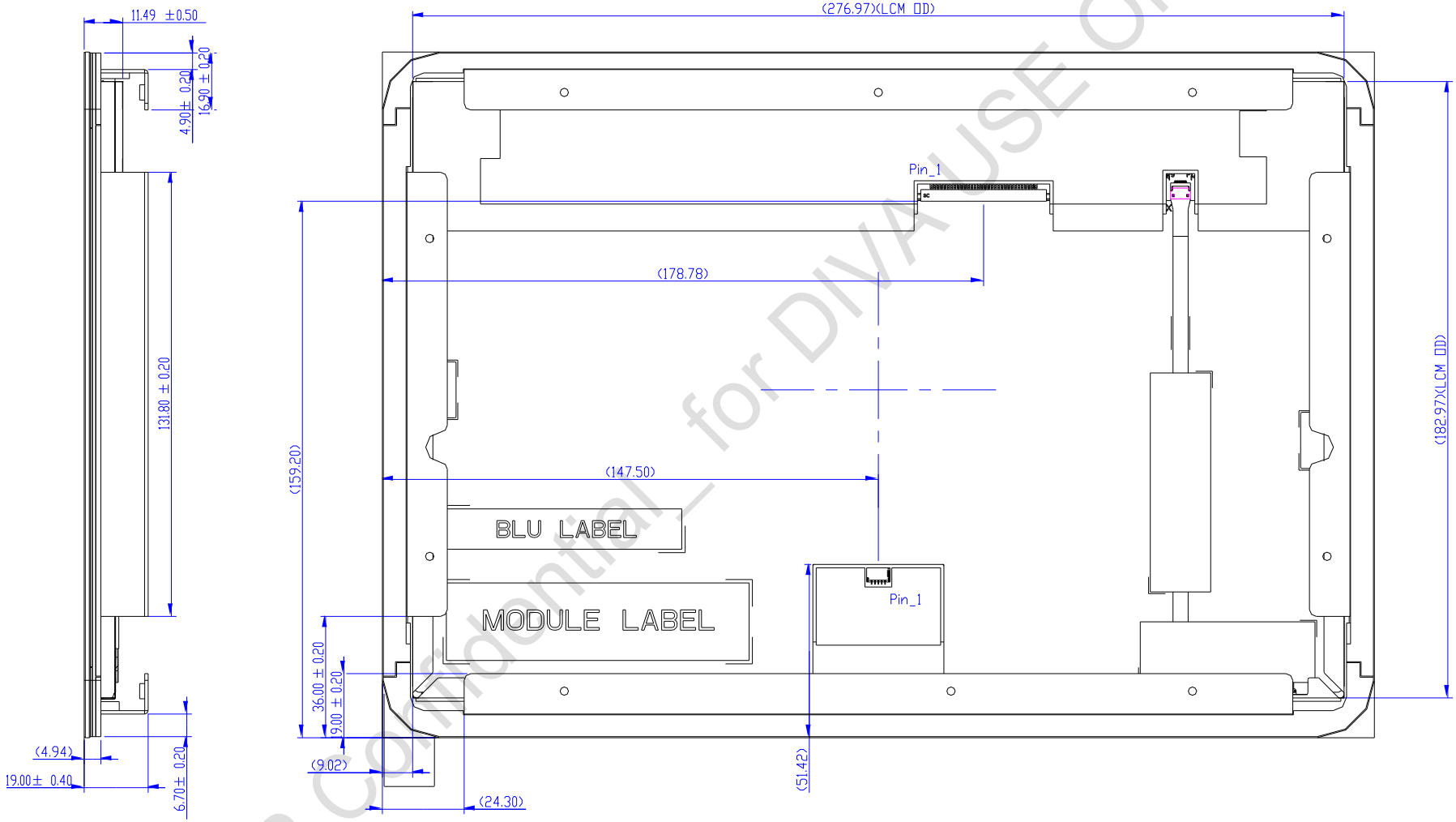
8.2 LCM Outline Dimension (Rear View)



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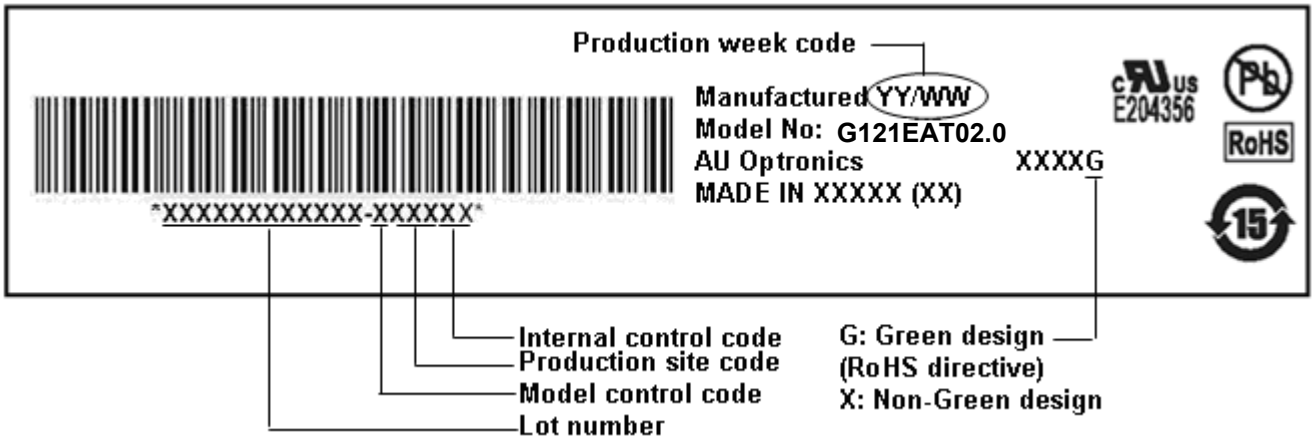
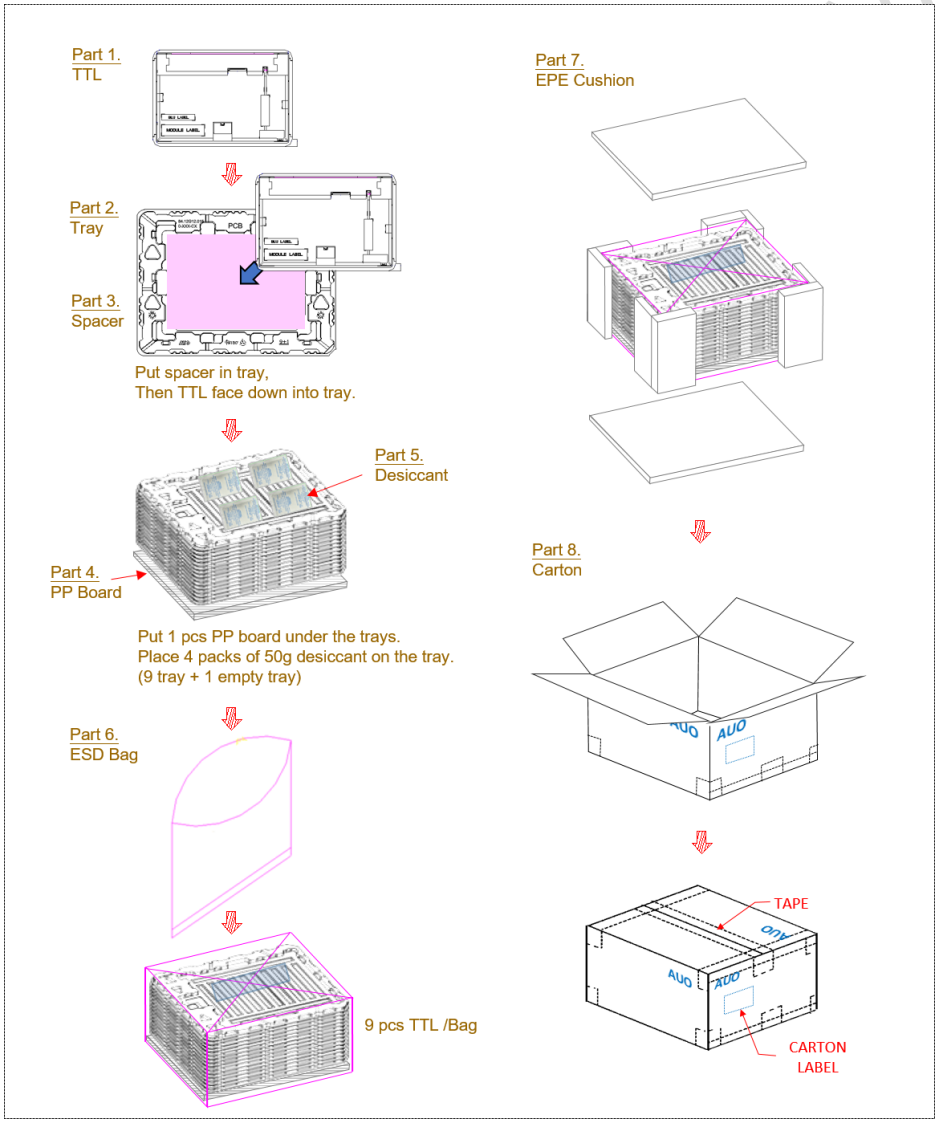
9. Label and Packaging

9.1 Shipping Label (on the rear side of TFT-LCD display)



9.2 Carton Package



10 Safety

10.1 Sharp Edge Requirements

There will be no sharp edges or comers on the display assembly that could cause injury.

10.2 Materials

10.2.1 Toxicity

There will be no carcinogenic materials used anywhere in the display module. If toxic materials are used, they will be reviewed and approved by the responsible AUO toxicologist.

10.2.2 Flammability

All components including electrical components that do not meet the flammability grade UL94-V1 in the module will complete the flammability rating exception approval process.

The printed circuit board will be made from material rated 94-V1 or better. The actual UL flammability rating will be printed on the printed circuit board.

10.3 Capacitors

If any polarized capacitors are used in the display assembly, provisions will be made to keep them from being inserted backwards.

10.4 National Test Lab Requirement

The display module will satisfy all requirements for compliance to:

UL 1950, First Edition	U.S.A. Information Technology Equipment

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